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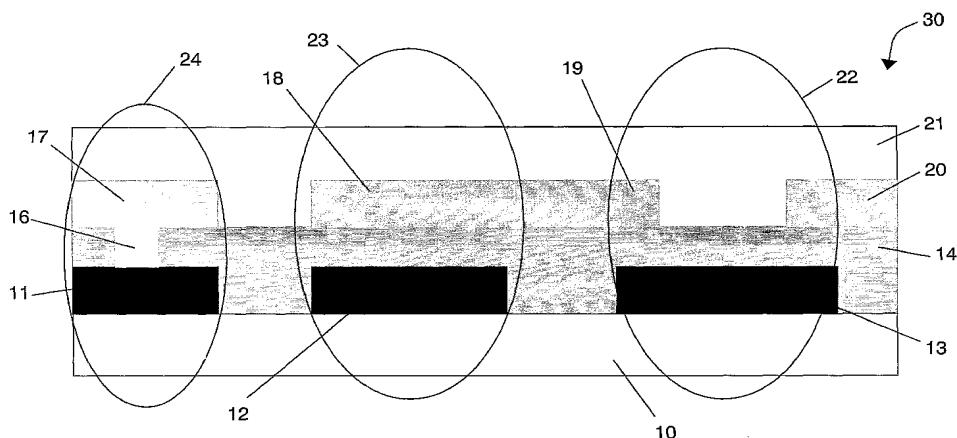
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(54) Title: METHOD FOR THE MANUFACTURE OF A NON-VOLATILE FERROELECTRIC MEMORY DEVICE AND MEMORY DEVICE THUS OBTAINED



(57) Abstract: The present invention relates to non-volatile ferroelectric memory devices (30) comprising a transistor (22) and a capacitor (23), and more particularly to non-volatile electrically erasable programmable ferroelectric memory elements, and a method for processing such non-volatile ferroelectric memory devices (30). The method according to the invention comprises a limited number of mask steps because a gate dielectric layer of the transistor (22) and a dielectric layer of the capacitor (23) are made from the same organic or inorganic ferroelectric layer (14).

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